B. AMENDMENTS TO THE CLAIMS

1. (canceled)

2. (canceled)

3. (canceled)

4. (canceled)

5. (canceled)

6. (canceled)

7. (canceled)

8. (original) A method for dynamically assigning interface pins, said method comprising:
receiving a first assignment request;

identifying one or more interface pins that correspond to the first assignment request;

selecting a first interface controller from a plurality of interface controllers that correspond to the first assignment request; and

associating the identified interface pins with the selected interface controller.

9. (original) The method as described in claim 8 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.

10. (original) The method as described in claim 8 further comprising:

receiving a second assignment request, the second assignment request corresponding to the identified interface pins;

selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and

re-associating the identified interface pins to the second interface controller.

11. (original) The method as described in claim 8 wherein the associating is performed using a look-up table.

12. (original) The method as described in claim 8 further comprising:

determining whether there are more interface pins that are not associated with the first interface controller; and

assigning the non-associated interface pins to a second interface controller in response to the determination.

13. (original) The method as described in claim 8 further comprising:

receiving data from the identified interface pins; and

providing the data to the first interface controller.

14. (original) The method as described in claim 8 wherein the associating is performed at system initialization.

15. (original) An information handling system comprising:

one or more processors;

one or more interface pins;

a plurality of interface controllers;

a memory accessible by the processors;

one or more nonvolatile storage devices accessible by the processors; and

an interface pin assignment tool for assigning one or more of the interface pins to one of the interface controllers, the interface pin assignment tool including:

means for receiving a first assignment request;

means for identifying one or more of the interface pins that correspond to the first assignment request;

means for selecting a first interface controller from the plurality of interface controllers that correspond to the first assignment request; and

means for associating the identified interface pins with the selected interface controller.

16. (original) The information handling system as described in claim 15 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.

17. (original) The information handling system as described in claim 15 further comprising:

means for receiving a second assignment request, the second assignment request corresponding to the identified interface pins;

means for selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and

means for re-associating the identified interface pins to the second interface controller.

18. (original) The information handling system as described in claim 15 wherein the associating is performed using a look-up table.

19. (original) The information handling system as described in claim 15 further comprising:
means for determining whether there are more interface pins that are not associated with the first interface controller; and

means for assigning the non-associated interface pins to a second interface controller in response to the determination.

20. (original) The information handling system as described in claim 15 further comprising:
means for receiving data from the identified interface pins; and

means for providing the data to the first interface controller.

21. (original) A computer program product stored on a computer operable media for dynamically changing pin to interface controller assignment:
means for receiving a first assignment request;

means for identifying one or more interface pins that correspond to the first assignment request;

means for selecting a first interface controller from a plurality of interface controllers that correspond to the first assignment request; and

means for associating the identified interface pins with the selected interface controller.

22. (original) The computer program product as described in claim 21 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.

23. (original) The computer program product as described in claim 21 further comprising:
means for receiving a second assignment request, the second assignment request corresponding to the identified interface pins;

means for selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and

means for re-associating the identified interface pins to the second interface controller.

24. (original) The computer program product as described in claim 21 wherein the associating is performed using a look-up table.

25. (original) The computer program product as described in claim 21 further comprising:
means for determining whether there are more interface pins that are not associated with the first interface controller; and

means for assigning the non-associated interface pins to a second interface controller in response to the determination.

26. (original) The computer program product as described in claim 21 further comprising:
means for receiving data from the identified interface pins; and

means for providing the data to the first interface controller.

27. (original) The computer program product as described in claim 21 wherein the associating is performed at system initialization.